



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda

Art Unit: 2128

Serial No.: 09/943,595

Confirmation No.: 2423

Filed: August 30, 2001

Examiner: Akash Saxena

Docket: TI-30482

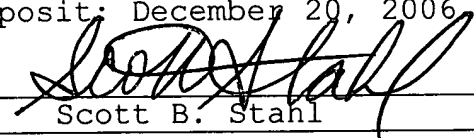
For: COLLECTING AND EXPORTING ON-CHIP DATA PROCESSOR TRACE AND
TIMING INFORMATION WITH DIFFERING COLLECTION AND EXPORT
FORMATS

Mail Stop RCE
Commissioner for Patents
P. O. Box 1450
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Date of Deposit: December 20, 2006

Signature: 

Scott B. Stahl

AMENDMENT UNDER 37 C.F.R. 1.111

Dear Sir:

This paper is a submission accompanying a Request for
Continued Examination in this application, and is responsive to the
Office Action mailed on September 20, 2006.